

FIG. 3

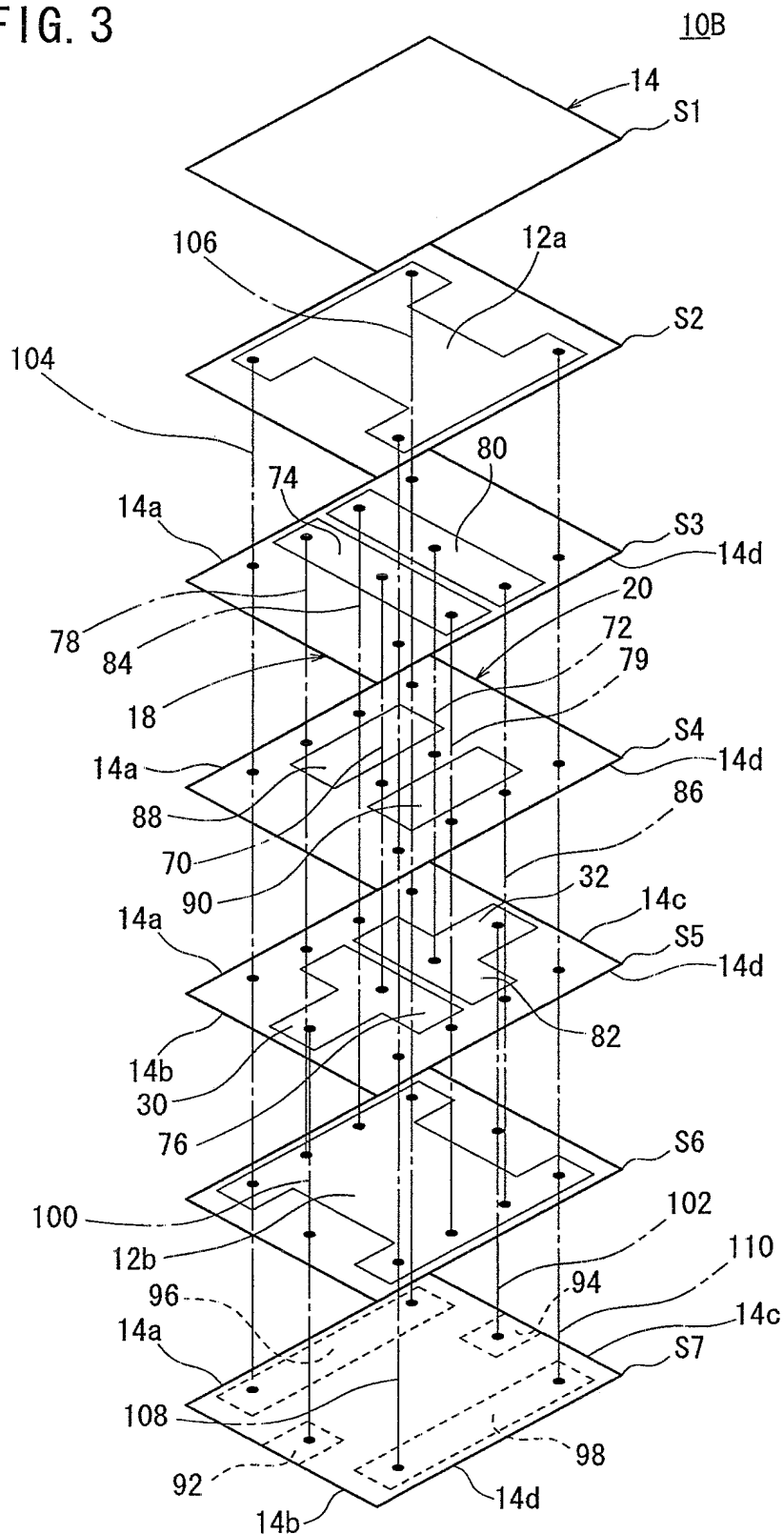


FIG. 4

FIG. 4 is a cross-sectional view of a semiconductor device. The device includes a substrate 60 with a top layer 62 and a bottom layer 64. A central region 66 contains a series of vertical structures. On the left, a stack of layers 104 (108) and 106 (110) is shown. A central vertical structure 70 is surrounded by a layer 72. To the right, a vertical structure 100 is shown. Various other layers and structures are labeled with numbers 14, 18, 20, 30, 32, 62, 64, 74, 76, 78 (79), 80, 84 (86), 92, 94, 96 (98), and 12a, 12b.

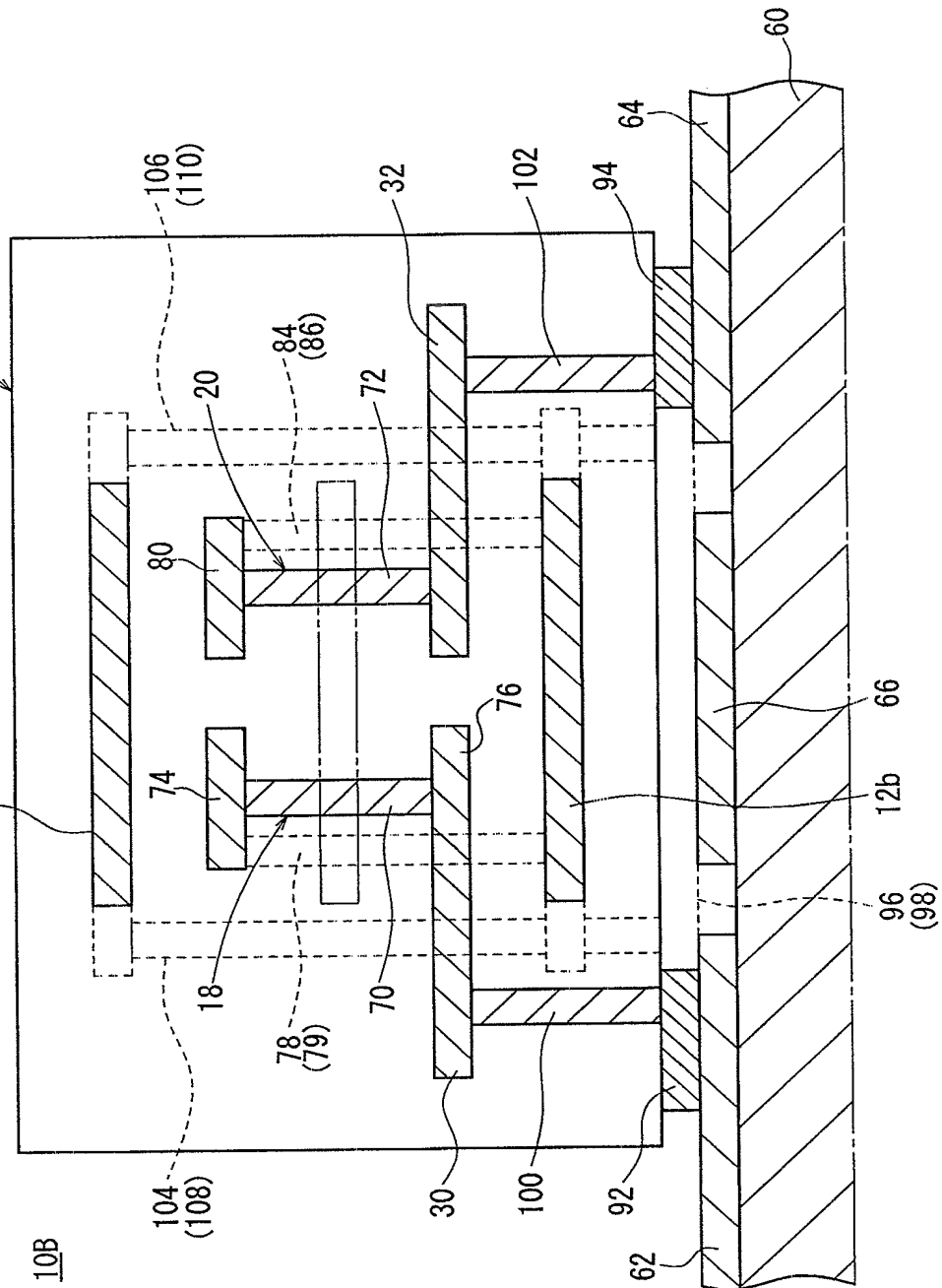


FIG. 5

